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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

MOS FIELD EFFECT TRANSISTOR NP82N055PUG

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The NP82N055PUG is N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Channel temperature 175 degree rating
- Super low on-state resistance
- $R_{DS(on)}$ = 5.2 m Ω MAX. (V_{GS} = 10 V, I_D = 41 A)
- Low Ciss: Ciss = 6400 pF TYP.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	55	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±82	А
Drain Current (pulse) Note1	D(pulse)	±328	А
Total Power Dissipation (T _A = 25°C)	PT1	1.8	W
Total Power Dissipation (Tc = 25°C)	Pt2	143	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	–55 to +175	°C
Repetitive Avalanche Current Note2	lar	38	А
Repetitive Avalanche Energy Note2	Ear	144	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. T_{ch} \leq 150°C, V_{DD} = 28 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	1.05	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

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ORDERING INFORMATION

PART NUMBER	PACKAGE	
NP82N055PUG	TO-263 (MP-25ZP)	



(TO-263)

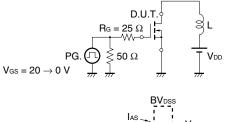
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

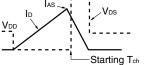
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	loss	V _{DS} = 55 V, V _{GS} = 0 V			1	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate to Source Threshold Voltage Note	V _{GS(th)}	V_{DS} = V_{GS} , I_D = 250 μ A	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 41 A	19	37		S
Drain to Source On-state Resistance Note	RDS(on)	Vgs = 10 V, Id = 41 A		4.1	5.2	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V		6400	9600	pF
Output Capacitance	Coss	V _{GS} = 0 V		465	700	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		275	500	pF
Turn-on Delay Time	td(on)	V _{DD} = 28 V, I _D = 41 A		40	90	ns
Rise Time	tr	V _{GS} = 10 V		93	240	ns
Turn-off Delay Time	td(off)	Rg = 0 Ω		72	150	ns
Fall Time	tr			10	30	ns
Total Gate Charge	QG	V _{DD} = 44 V		106	160	nC
Gate to Source Charge	Q _{GS}	V _{GS} = 10 V		29		nC
Gate to Drain Charge	Qgd	I _D = 82 A		35		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 82 A, VGS = 0 V		0.92	1.5	V
Reverse Recovery Time	trr	IF = 82 A, VGS = 0 V		42		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		57		nC

Note Pulsed

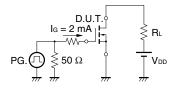
TEST CIRCUIT 1 AVALANCHE CAPABILITY

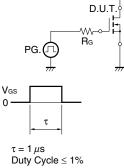
TEST CIRCUIT 2 SWITCHING TIME

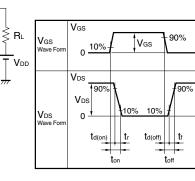




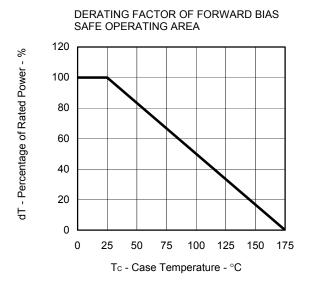
TEST CIRCUIT 3 GATE CHARGE

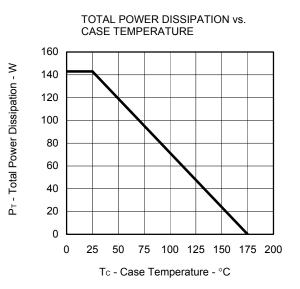




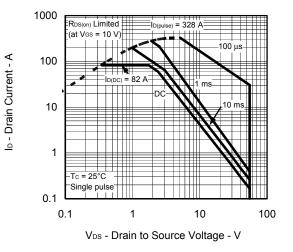


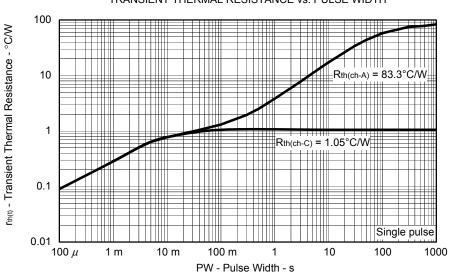
TYPICAL CHARACTERISTICS (TA = 25°C)



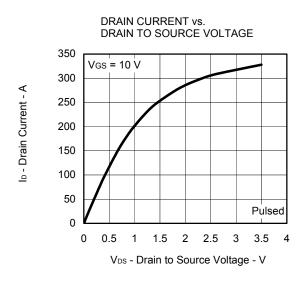


FORWARD BIAS SAFE OPERATING AREA

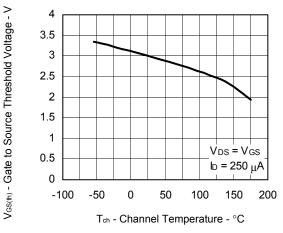




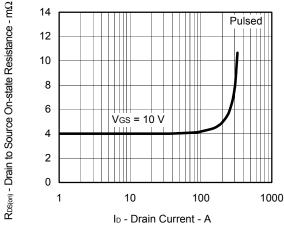
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



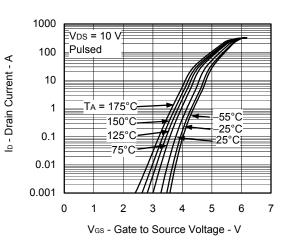
GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE

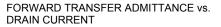


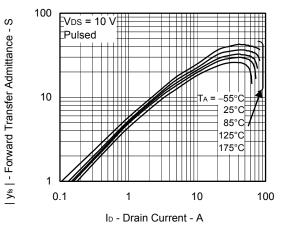
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



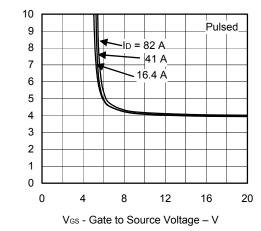
FORWARD TRANSFER CHARACTERISTICS



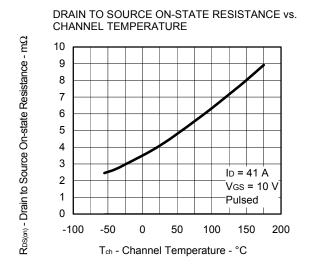




DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



RDS(on) - Drain to Source On-state Resistance - mΩ



SWITCHING CHARACTERISTICS

td(off)

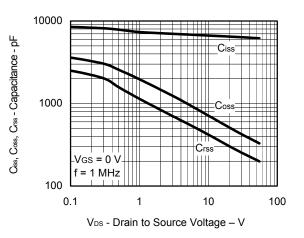
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td(on)

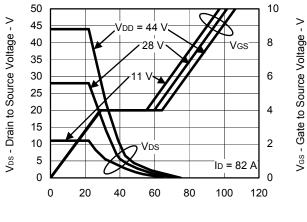
tf

100

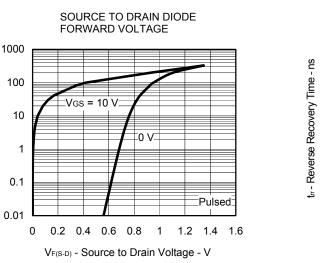
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



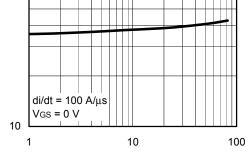
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



QG - Gate Charge - nC



REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT 100



IF - Diode Forward Current - A

td(on), tr, td(off), tr - Switching Time - ns

1000

100

10

1

0.1

Vdd = 28 V Vgs = 10 V

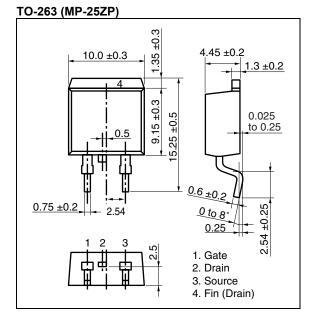
1

ID - Drain Current - A

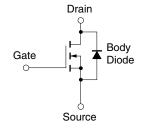
 $R_G = 0 \Omega$

IF - Diode Forward Current - A

PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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